



Mount Timer pcb centrally in back box using self adhesive pillars provided.  
 ##Please ensure cables enter##  
 ##back box above or below ##  
 ##timer pcb position##

Please note that all drawings and diagrams supplied for bespoke applications for RSS products are given as a free service to assist customers and no liability for errors and omissions can be accepted by RSS. Customers remain entirely responsible for the quality and correctness of all installation work.

ISS	DESCRIPTION	DATE	THE RAYTEL GROUP LTD	TITLE	DRAWN	CHECKED	APPROVED	DATE
1	INITIAL	30/01/17			Push to Exit Button and Timed Emergency Reset External wiring Detail	MF		
					DRAWING No. VFET2-1			